50Ω 1200 to 2200 MHz

The Big Deal

- · Low phase noise and spurious
- Robust design and construction
- Fast settling time



CASE STYLE: KL1294

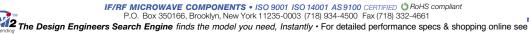
Product Overview

The DSN-2000A-219+ is a Frequency Synthesizer, designed to operate from 1200 to 2200 MHz for cable TV application. The DSN-2000A-219+ is packaged in a metal case (size of 1.25" x 1.00" x 0.20") to shield against unwanted signals and noise.

Key Features

Feature	Advantages
Low phase noise and spurious: • Phase Noise: -101 dBc/Hz typ. @ 10 kHz offset • Comparison Spurious: -82 dBc typ. • Reference Spurious: -85 dBc typ.	Low phase noise and spurious improve system EVM (Error Vector Magnitude).
Robust design and construction	To enhance the robustness of DSN-2000A-219+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.
Fast settling time. 0.5mSec typical	Settling time, 0.5mSec typical can be used for settling applications such as jammers etc.







Frequency Synthesizer

DSN-2000A-219+

50Ω 1200 to 2200 MHz

Features

- Integrated VCO + PLL
- Low phase noise and spurious
- · Robust design and construction
- Operating voltage (VCC VCO=+10V, VCC PLL=+22V)
- · Fast settling time

Applications

Cable TV



CASE STYLE: KL1294 PRICE: \$45.95 ea. QTY (1-9)

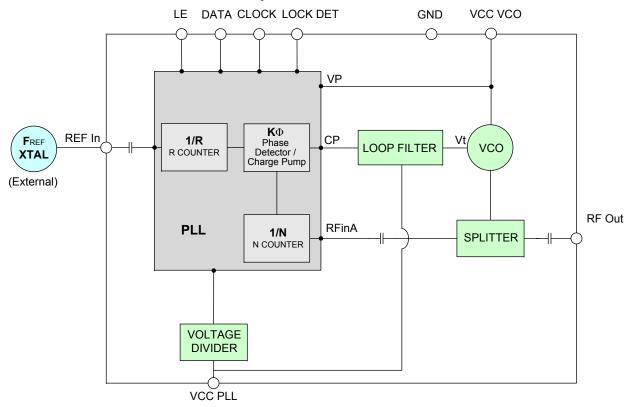
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

General Description

The DSN-2000A-219+ is a Frequency Synthesizer, designed to operate from 1200 to 2200 MHz for cable TV application. The DSN-2000A-219+ is packaged in a metal case (size of 1.25" x 1.00" x 0.20") to shield against unwanted signals and noise. To enhance the robustness of DSN-2000A-219+, each internal component is secured to the substrate with chip bonder, thereby eliminating the risk of tombstoning during subsequent solder reflow operations by the customer.

Simplified Schematic



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Electrical Specifications (over operating temperature -40°C to +85°C)

Parameters	Test Conditions	Min.	Тур.	Max.	Units	
Frequency Range		-	1200	-	2200	MHz
Step Size		-	- 10 -		MHz	
Settling Time		Within ± 1 kHz	-	0.5	-	mSec
Output Power		-	+5	+9	+11	dBm
		@ 100 Hz offset	-	-84	-	
		@ 1 kHz offset	-	-101	-94	
SSB Phase Noise		@ 10 kHz offset	-	-101	-93	dBc/Hz
		@ 100 kHz offset	-	-107	-102	
		@ 1 MHz offset	-	-137	-131	
Integrated SSB Phase Noise		@ 10kHz - 3MHz	-	-54	-	dBc
Reference Spurious Suppress	sion	Ref. Freq. 20 MHz	-	-85	-70	
Comparison Spurious Suppre	ssion	Step Size 10 MHz	-	-82	-65	dBc
Non - Harmonic Spurious Sup	pression	-	-	-90	-	ubc
Harmonic Suppression		-	-	-27	-11	
VCO Supply Voltage		+10.00	+9.75	+10.00	+10.25	V
PLL Supply Voltage		+22.00	+21.75	+22.00	+22.25	V
VCO Supply Current		-	-	53	59	mA
PLL Supply Current		-	-	18	26	lliA
	Frequency	20 (square wave)	-	20	-	MHz
Reference Input	Amplitude	1	-	1	-	V _{P-P}
(External)	Input impedance	-	-	100	-	ΚΩ
	Phase Noise @ 1 kHz offset	-	-	-145	-	dBc/Hz
RF Output port Impedance		-	-	50	-	Ω
Input Logic Level	Input high voltage	-	2.65	-	-	V
Input Logic Level	Input low voltage	-	-	-	0.65	V
Digital Look Datast	Locked	-	2.15	-	2.60	V
Digital Lock Detect	Unlocked	-	-	-	0.40	V
Frequency Synthesizer PLL	Frequency Synthesizer PLL - ADF4106					
PLL Programming		-	3-wire seria	al 3.3V CMO	S	
	F_Register *	-	(MSB) 0002	XYZXYZ0000	0000001001	10 (LSB)
Register Map @ 2200 MHz	N_Register	-	(MSB) 001000000001101100010001 (LSB)			
	R_Register	-	(MSB) 000	10000000000	00000001000	(LSB)

* Refer to Charge Pump Settings

ricio: io cila: go : allip collingo									
FREQ.LOCK [MHz]	Charge Pump Settings								
THEG.EGGR [MH2]	X	Υ	Z						
1200 - 1390	0	1	1						
1400 - 1850	1	0	0						
1860 - 2040	1	0	1						
2050 - 2140	1	1	0						
2150 - 2200	1	1	1						

Absolute Maximum Ratings

Parameters	Ratings
VCO Supply Voltage	11V
PLL Supply Voltage	23V
VCO Supply Voltage to PLL Supply Voltage	N.A
Reference Frequency Voltage	-0.3Vmin, +3.6Vmax
Data, Clock, LE Levels	-0.3Vmin, +3.6Vmax
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +100°C

Permanent damage may occur if any of these limits are exceeded



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Typical Performance Data

FREQUENCY	РО	WER OUTP	UT	VCO CURRENT			PLL CURENT			
(MHz)		(dBm)			(mA)			(mA)		
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	
1200	9.45	9.28	8.90	52.33	53.12	54.10	17.69	18.18	20.21	
1280	9.50	9.32	8.95	52.42	53.20	54.19	17.77	18.26	20.31	
1380	9.65	9.45	9.04	52.53	53.35	54.26	17.94	18.44	20.51	
1480	9.58	9.36	8.95	52.66	53.46	54.37	18.09	18.60	20.68	
1580	9.46	9.28	8.84	52.73	53.53	54.43	18.21	18.73	20.82	
1680	9.28	9.14	8.72	52.78	53.57	54.46	17.31	18.60	20.68	
1780	9.02	8.93	8.57	52.79	53.60	54.49	17.50	18.80	20.90	
1880	8.81	8.76	8.43	52.86	53.65	54.56	17.63	18.93	21.04	
1980	8.53	8.54	8.27	52.85	53.65	54.59	17.77	19.07	21.19	
2080	8.35	8.41	8.14	52.86	53.70	54.64	17.65	18.95	21.06	
2200	8.02	8.09	7.83	52.81	53.68	54.66	17.89	19.19	21.32	

FREQUENCY	HARMONICS (dBc)							
(MHz)		F2			F3			
	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C		
1200	-14.97	-18.68	-24.08	-20.39	-17.67	-16.72		
1280	-17.00	-21.57	-25.77	-21.57	-18.63	-17.93		
1380	-18.61	-22.36	-27.97	-22.91	-20.42	-20.49		
1480	-21.70	-27.54	-26.77	-25.73	-23.81	-24.26		
1580	-21.50	-27.04	-26.66	-29.70	-27.68	-28.25		
1680	-24.23	-28.81	-23.42	-37.22	-34.85	-35.23		
1780	-24.63	-28.80	-23.72	-48.67	-48.10	-46.86		
1880	-24.07	-34.22	-25.11	-42.14	-40.38	-42.21		
1980	-26.82	-29.22	-23.66	-35.28	-35.13	-37.75		
2080	-26.62	-31.11	-26.60	-34.21	-33.52	-34.98		
2200	-26.54	-31.56	-28.60	-32.14	-31.37	-33.01		



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EDECHENCY	PHASE NOISE (dBc/Hz) @OFFSETS							
FREQUENCY (MHz)	+25°C							
, ,	100Hz	1kHz	10kHz	100kHz	1MHz			
1200	-88.73	-103.90	-104.11	-108.84	-137.54			
1280	-90.12	-103.60	-102.80	-108.90	-137.55			
1380	-91.60	-100.70	-101.48	-109.08	-137.42			
1480	-89.32	-100.76	-102.53	-107.10	-137.24			
1580	-88.52	-101.26	-101.69	-107.74	-137.19			
1680	-89.88	-100.82	-102.32	-107.46	-137.30			
1780	-90.70	-99.20	-101.03	-107.62	-137.11			
1880	-85.84	-98.64	-100.26	-107.29	-137.20			
1980	-89.03	-97.25	-99.00	-108.25	-137.21			
2080	-84.58	-97.37	-99.75	-107.51	-137.28			
2200	-87.23	-97.91	-99.14	-108.12	-137.39			

	PHA	ASE NOIS	E (dBc/Hz	e) @OFFS	ETS	
FREQUENCY (MHz)	-45°C					
	100Hz	1kHz	10kHz	100kHz	1MHz	
1200	-89.61	-104.25	-104.31	-107.58	-138.74	
1280	-91.04	-104.72	-102.45	-108.42	-138.73	
1380	-86.84	-102.74	-100.68	-109.39	-138.45	
1480	-90.01	-104.25	-102.64	-106.97	-138.13	
1580	-89.40	-101.43	-101.36	-107.45	-137.95	
1680	-87.37	-100.95	-100.10	-107.86	-137.75	
1780	-90.94	-99.63	-100.47	-108.08	-137.73	
1880	-85.96	-98.87	-100.02	-107.37	-137.51	
1980	-86.59	-100.43	-99.46	-107.32	-137.55	
2080	-88.41	-100.95	-98.87	-107.01	-137.55	
2200	-88.46	-99.67	-98.89	-107.09	-137.76	

FREQUENCY	PHASE NOISE (dBc/Hz) @OFFSETS							
(MHz)	+85°C							
, ,	100Hz	1kHz	10kHz	100kHz	1MHz			
1200	-92.45	-104.22	-103.36	-106.84	-135.14			
1280	-88.99	-103.73	-101.32	-107.67	-135.11			
1380	-90.00	-103.14	-101.80	-107.30	-135.33			
1480	-88.79	-101.95	-101.33	-105.69	-135.41			
1580	-88.34	-102.98	-101.22	-106.11	-135.61			
1680	-88.11	-102.53	-100.56	-106.28	-135.67			
1780	-87.73	-101.13	-99.80	-107.14	-135.86			
1880	-86.50	-100.29	-98.88	-106.09	-135.88			
1980	-86.48	-100.22	-98.73	-106.79	-136.12			
2080	-87.30	-100.17	-97.47	-106.86	-136.31			
2200	-86.78	-100.12	-98.23	-107.32	-136.44			



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COMPARISON SPURIOUS ORDER	COMPARISON SPURIOUS @ Fcarrier 1200MHz+(n*Fcomparison) (dBc) note 1			COMPARISON SPURIOUS @ Fcarrier 1700MHz+(n*Fcomparison) (dBc) note 1			ARISON SPI @Fcarrier z+(n*Fcom (dBc) no	parison)	
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-90.90	-92.34	-94.73	-87.13	-90.15	-91.04	-83.66	-85.37	-87.94
-4	-87.00	-88.76	-91.17	-84.37	-88.83	-89.48	-81.34	-84.53	-86.50
-3	-86.06	-90.25	-90.99	-84.75	-89.99	-91.10	-81.12	-85.44	-86.92
-2	-84.40	-91.41	-91.80	-84.49	-90.00	-90.61	-80.66	-85.65	-86.01
-1	-84.60	-88.17	-89.08	-81.60	-86.87	-87.09	-80.48	-84.49	-85.02
0 ^{note 2}	-	-	-	-	-	-	-	-	-
+1	-79.29	-80.65	-86.25	-74.32	-80.77	-81.53	-74.18	-78.71	-82.94
+2	-83.39	-86.01	-89.73	-78.47	-84.51	-85.33	-77.67	-82.40	-85.18
+3	-84.93	-89.23	-90.67	-82.20	-87.19	-87.99	-80.63	-85.31	-86.73
+4	-86.16	-92.50	-91.53	-84.73	-88.55	-89.42	-82.38	-86.45	-87.19
+5	-88.76	-93.00	-91.35	-88.35	-90.05	-90.72	-85.48	-87.99	-87.76

Note 1: Comparison frequency 10 MHz

Note 2: All spurs are referenced to carrier signal (n=0).

REFERENCE SPURIOUS ORDER	REFERENCE SPURIOUS @Fcarrier 1200MHz+(n*Freference) (dBc) note 3			REFERENCE SPURIOUS @Fcarrier 1700MHz+(n*Freference) (dBc) note 3			RENCE SPU @Fcarrier Hz+(n*Frefe (dBc) no	erence)	
n	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C	-45°C	+25°C	+85°C
-5	-106.42	-101.16	-99.36	-95.98	-106.00	-95.79	-103.03	-100.94	-100.91
-4	-116.13	-102.13	-102.70	-111.63	-99.77	-104.16	-98.73	-93.51	-100.19
-3	-94.72	-91.64	-93.89	-89.75	-90.05	-91.15	-86.17	-85.83	-88.35
-2	-86.89	-88.79	-91.22	-84.55	-88.85	-89.44	-81.33	-84.50	-86.44
-1	-84.49	-91.43	-91.94	-84.46	-90.06	-90.49	-80.59	-85.67	-86.02
O ^{note 4}	-	-		-	-	-	-	-	-
+1	-82.01	-86.01	-89.83	-78.57	-84.53	-85.32	-77.77	-82.41	-85.14
+2	-86.14	-92.56	-91.66	-84.64	-88.54	-89.42	-82.43	-86.43	-87.24
+3	-90.05	-95.67	-92.29	-91.80	-91.71	-92.04	-88.25	-89.04	-87.99
+4	-94.98	-103.74	-94.28	-102.92	-96.23	-97.75	-99.75	-95.09	-93.40
+5	-105.18	-101.88	-102.61	-110.28	-102.73	-105.02	-105.06	-108.27	-99.66

Note 3: Reference frequency 20 MHz

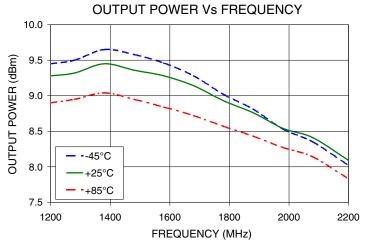
Note 4: All spurs are referenced to carrier signal (n=0).

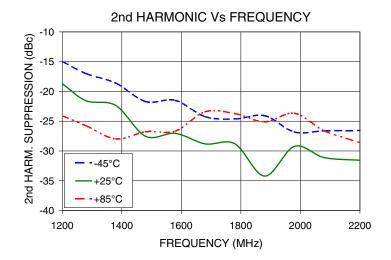


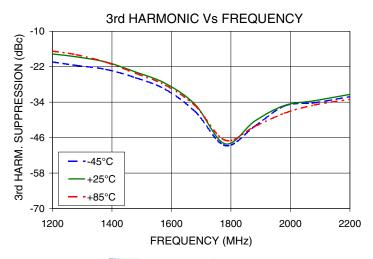
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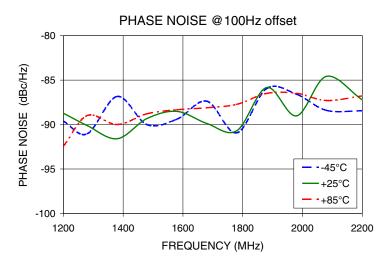
Typical Performance Curves

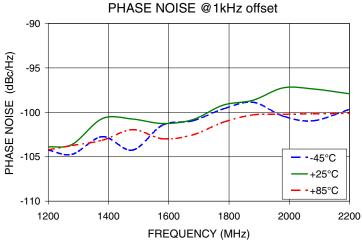


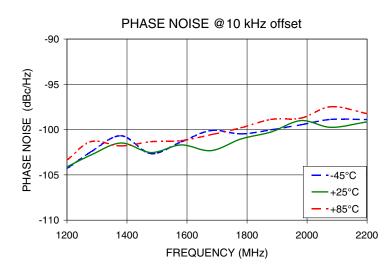


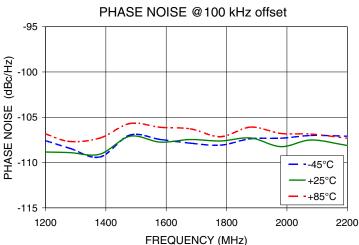


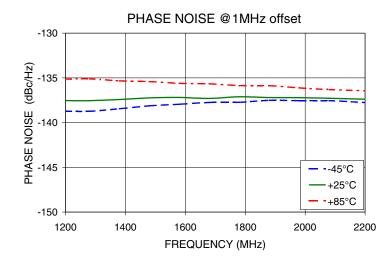
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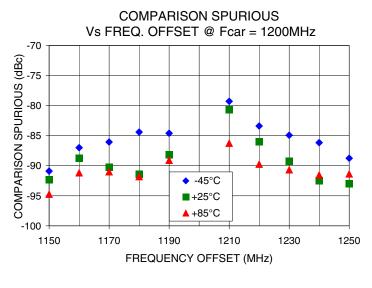
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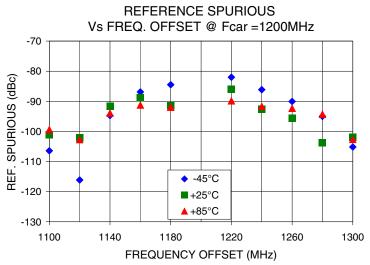
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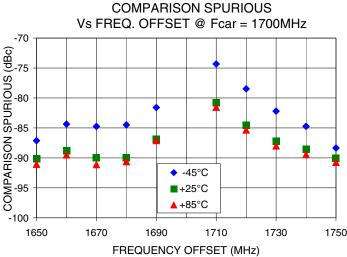
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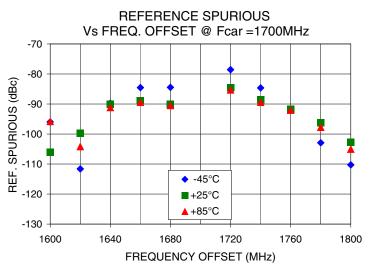
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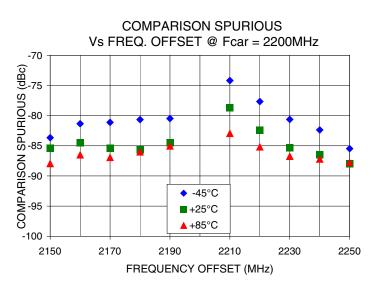


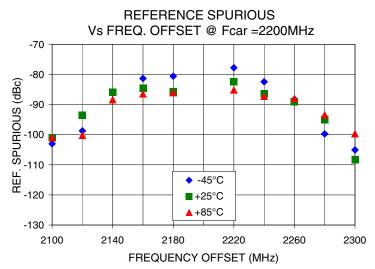












III Mini-Circuits

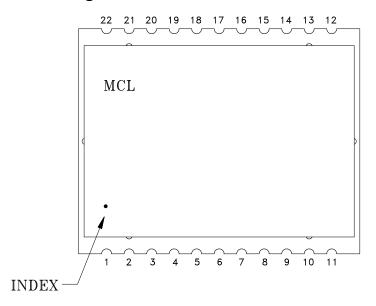
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Pin Configuration

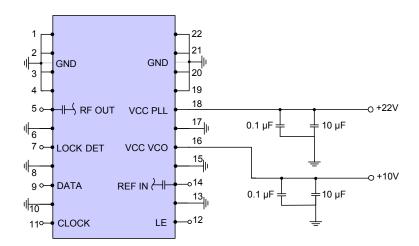


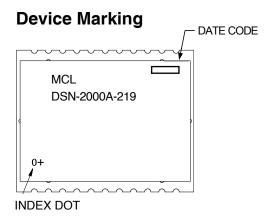
Pin Connection

Pin Number	Function	Pin Number	Function
1	GND	12	LE
2	GND	13	GND
3	GND	14	REF IN
4	GND	15	GND
5	RF OUT	16	VCC VCO
6	GND	17	GND
7	LOCK DET	18	VCC PLL
8	GND	19	GND
9	DATA	20	GND
10	GND	21	GND
11	CLOCK	22	GND

Recommended Application Circuit

Note: REF IN and RF OUT ports are internally AC coupled.





Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Case Style: KL1294

Tape & Reel: TR-F97

Suggested Layout for PCB Design: PL-318

Evaluation Board: TB-553+

Environment Ratings: ENV03T2

